



HDL Programming Fundamentals: VHDL and Verilog (Davinci Engineering)

By Nazeih M Botros

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Advances in semiconductor technology continue to increase the power and complexity of digital systems. To design such systems requires a strong knowledge of Application Specific Integrated Circuits (ASICs) and Field Programmable Gate Arrays (FPGAs), as well as the CAD tools required. Hardware Description Language (HDL) is an essential CAD tool that offers designers an efficient way for implementing and synthesizing the design on a chip. HDL Programming Fundamentals: VHDL and Verilog teaches students the essentials of HDL and the functionality of the digital components of a system. Unlike other texts, this book covers both IEEE standardized HDL languages: VHDL and Verilog. Both of these languages are widely used in industry and academia and have similar logic, but are different in style and syntax. By learning both languages students will be able to adapt to either one, or implement mixed language environments, which are gaining momentum as they combine the best features of the two languages in the same project. The text starts with the basic concepts of HDL, and covers the key topics such as data flow modeling, behavioral modeling, gate-level modeling, and advanced programming. Several comprehensive projects are included to show HDL in practical application, including examples of digital logic design, computer architecture, modern bioengineering, and simulation.

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Editorial Review

About the Author

Nazeih M. Botros (Carbondale, IL) is a professor in the Department of Electrical and Computer Engineering at the University of Southern Illinois. He has a Ph.D. in electrical engineering from the University of Oklahoma, and over 25 years of teaching experience in areas including Instrumentation, Artificial Intelligence, Computer Architecture, Digital Hardware Design, HDL, and Digital Signal Analysis. He has published articles in Journal of Control & Intelligent Systems, International Journal of Robotics and Automation, International Journal of Modeling and Simulation, and International Journal of Microcomputer Applications, and has spoken at various industry conferences.

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